

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/630,571	LEAM ET AL.	
Examiner		Art Unit		Page 1 of 1
Thao P. Le		2818		

**U.S. PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A US-6,232,179	05-2001	Sato, Kazuo	438/257
*	B US-6,501,134	12-2002	Krivokapic, Zoran	257/350
*	C US-2004/0029345	02-2004	Deleonibus et al.	438/257
*	D US-2002/0102793	08-2002	Wu, Ching-Yuan	438/257
	E US-5,933,730	08-1999	Sun et al.	438/258
	F US-6,693,013	02-2004	Bae et al.	438/303
	G US-6,624,024	09-2003	Prall et al.	438/257
	H US-			
	I US-			
	J US-			
	K US-			
	L US-			
	M US-			

**FOREIGN PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				
	O				
	P				
	Q				
	R				
	S				
	T				

**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	S. Wolf, "Silicon Processing for the VLSI era", Lattice Press, Vol. 4, pp. 603-615	
	V		
	W		
	X		

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.